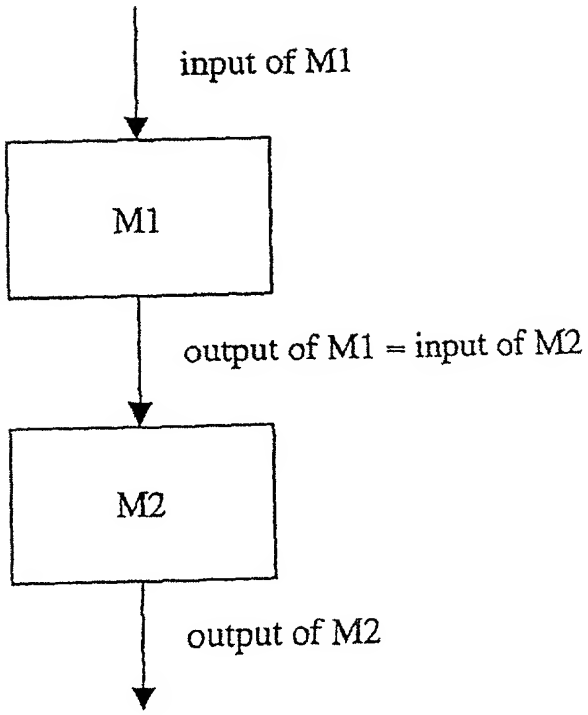
input of M1
M1
output of M1 = input of M2
M2
output of M2

FIG.1

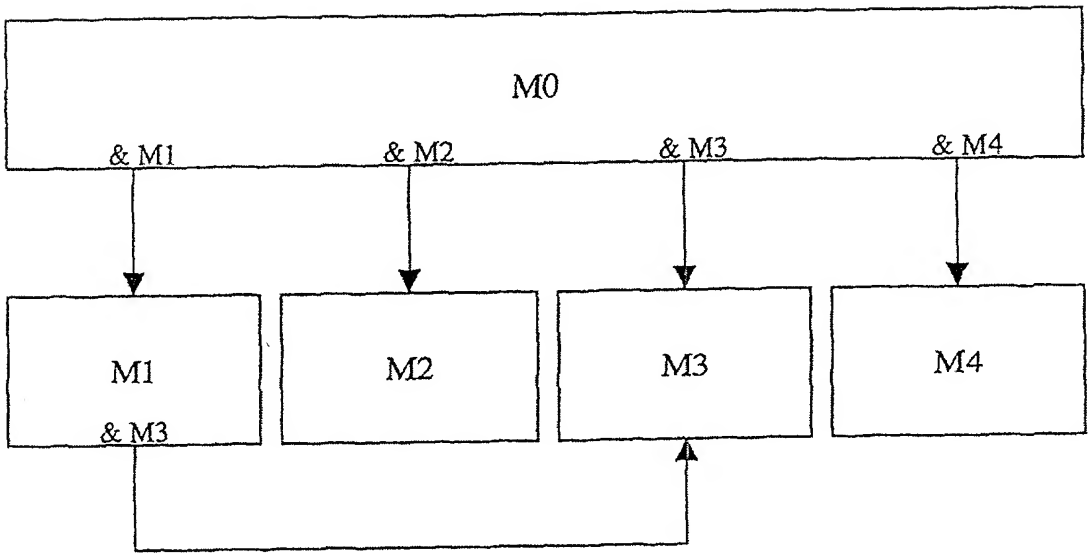
M0
& M1
& M2
& M3
& M4
M1
& M3
M2
M3
M4

FIG.2

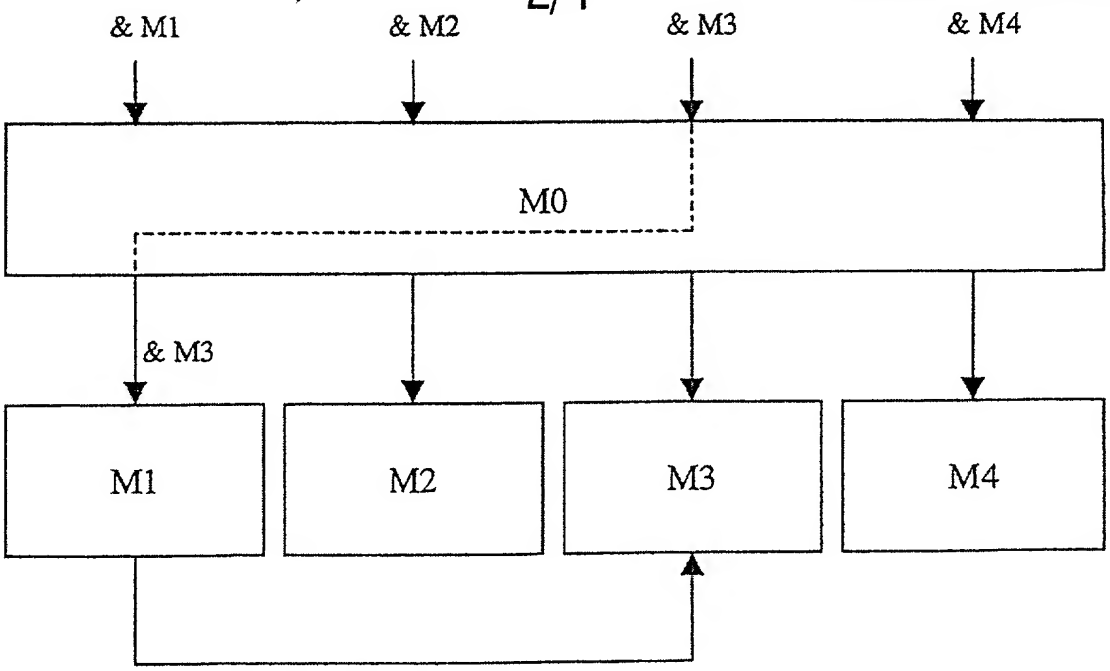

FIG.3

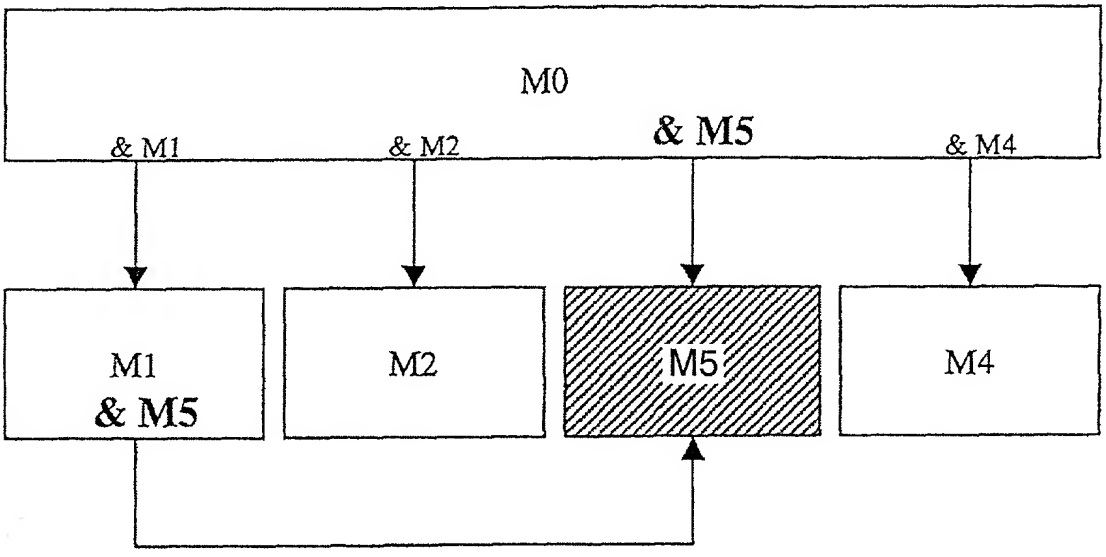

FIG.4

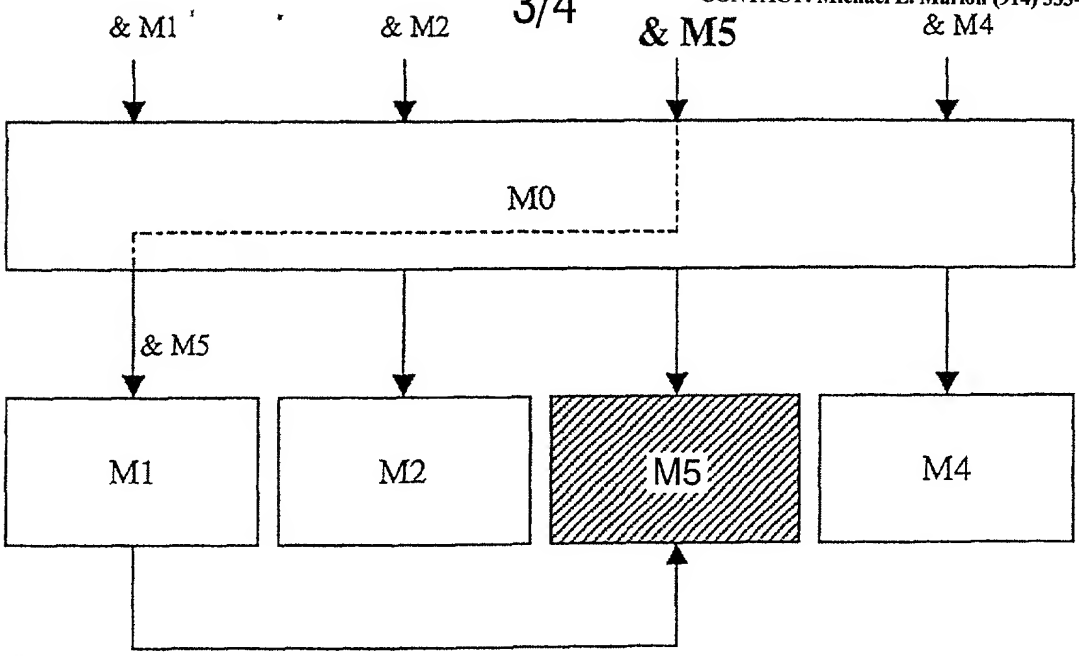

FIG.5

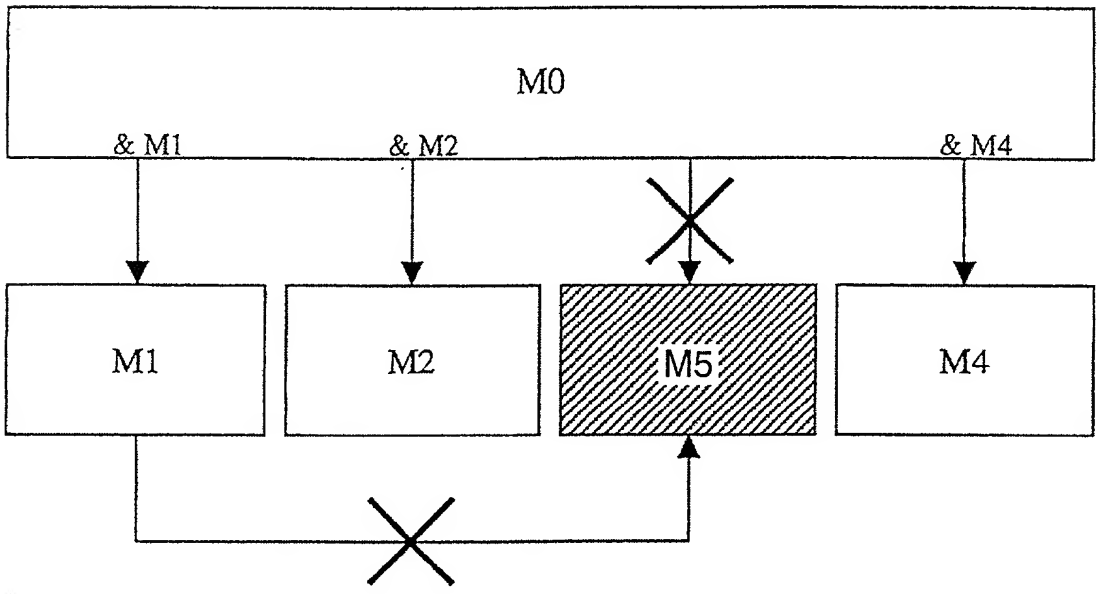

FIG.6

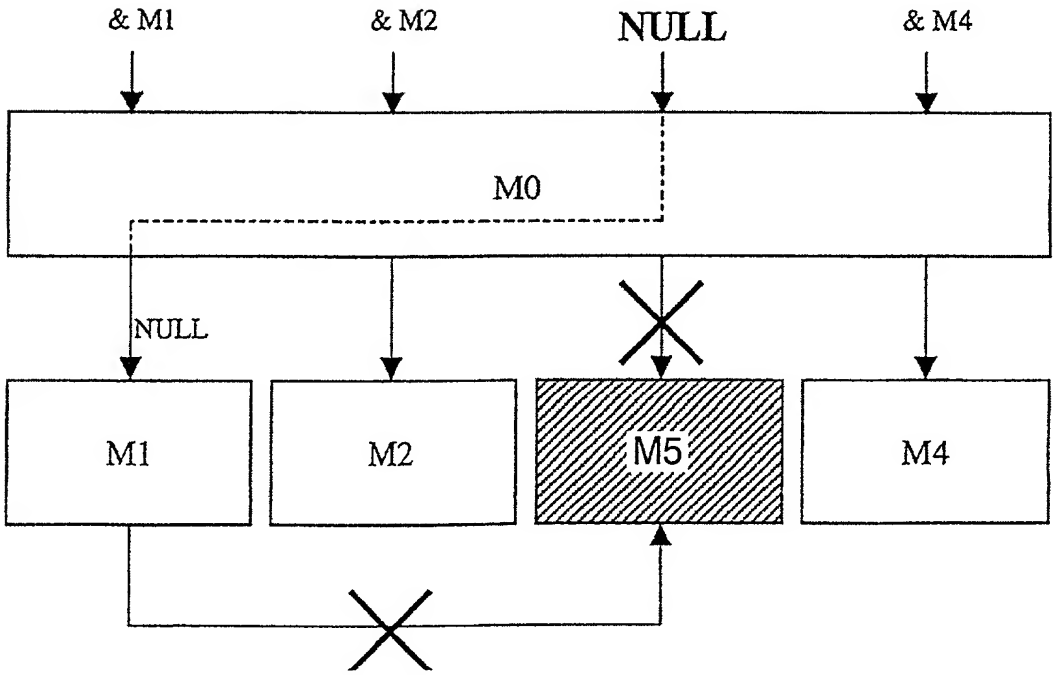
& M1
& M2
NULL
& M4
M0
NULL
M1
M2
M5
M4

FIG.7

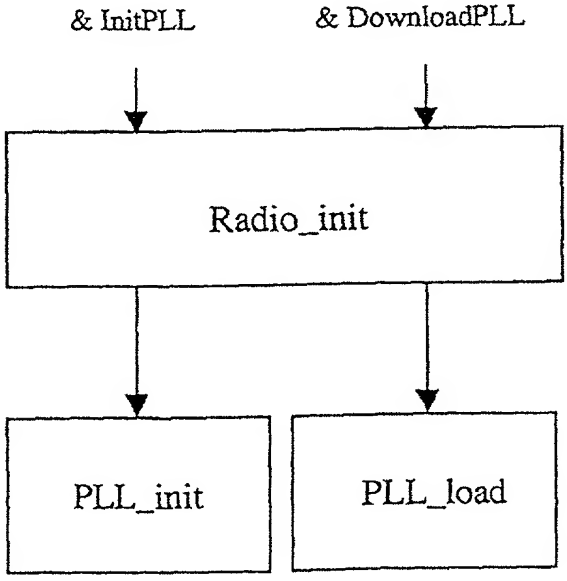
& InitPLL
& DownloadPLL
Radio_init
PLL_init
PLL_load

FIG.8